

**IN THE SPECIFICATION:**

Please replace the paragraphs at page 56, Ins. 9-22 with the following amended paragraphs:

Further, Fig. 10B is an example of a case in which the current supply line ~~3808~~ 3807 is formed parallel to the gate wiring 3801. Note that in Fig. 10B, the structure is formed such that the electric current supply line ~~3808~~ 3807 and the gate wiring 3801 not to overlap through an insulating film. In such a case that both wirings are formed in different layer, they can be provided to overlap each other via an insulating film. In this case, the exclusive surface area can be shared by the electric current supply line ~~3808~~ 3807 and the gate wiring 3801, and the pixel portion can be made with higher definition.

Furthermore, Fig. 10C is characterized in that the current supply line ~~3808~~ 3807 and the gate wiring 3801 are formed in parallel, similar to the structure of Fig. 10B, and additionally, in that the two pixels are formed so as to have linear symmetry around the current supply line ~~3808~~ 3807. In addition, it is effective to form the current supply line ~~3808~~ 3807 so as to overlap with one of the gate wirings 3801. In this case, the number of electric current supply lines can be reduced, and therefore the pixel portion can be made with higher definition